Registration Form

Title of the Workshop: Analog and Digital VLSI Design Flow and Embedded System Design

Workshop Date: July 4, 2022, to July 13, 2022

Sponsored by: "Karyashala (High-end Workshops)" scheme under "Accelerate Vigyan" program sponsored by SERB, Government of India.

Support: There will be a selection interview for the candidates. After selection the participants should deposit the registration fee. The TA, accommodation and foods will be borne by the Host Institute through SERB funding support.

Last date for Registration: 27 June 2022

Confirmation of Final Selection: 30 June 2022

For Registration please fill up the Google Form:

https://forms.gle/FTMAMbRRe6YwLWBYA