



NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India]

Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008

SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

NITR / SR / 2025 / Advt.- 24CS021 / L / 131

dt. 30-Mar-2026 Ref: FTS 260326-1831 SR-R03

Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.			
1.	Title of the Project:	Information Security Education and Awareness project Phase III (Hardware Security)	
2.	Project Code, Dept. with Closing Date:	SR/24/CS/021	31-Mar-2029 Computer Science and Engg.
3.	Funding Agency Details:	4197 - CBSD, MEITY [RBI - 10687701200]	-
4.	PI details:	Prof. SHYAMAPADA MUKHERJEE 9434663383	mukherjees@nitrrkl.ac.in, 0661-246 2357 - @ gmail.com
5.	Co-PI details:	Prof. SUJATA MAHANTY 9437240147	sujatam@nitrrkl.ac.in, 0661-246 2363 - @ gmail.com
6.	Details of the Post(s):		
Junior Research Fellow (JRF) <i>[Name of the Post(s)]</i>		7. Educational Qualification & Working Knowledge	
02 (Two) <i>[No. of Post(s)]</i>	01 Year(s), 00 Month(s) <i>(Tenure of Post)</i>	Essential Qualification (s): M.Tech./M.E./M.S.(R)/M.Tech(R) in Computer Science/Information Technology/Electronics & Communication Engineering with minimum 60% marks or 6.5 CGPA at both B.Tech./MCA/MSc and M.Tech. levels. OR B.Tech./B.E. in CSE/IT/ECE with a valid GATE score and CGPA ≥ 8.0 or marks ≥ 70%. OR M.Sc. (CS/IT/EC) with first class and valid GATE/NET qualification. OR MCA with first class and valid GATE/NET score.	
Year 1 & 2		Desirable Qualification:	
INR 37,000.00 /- per month (+) HRA @ NA % (if applicable)		Familiarity with concepts such as cryptographic algorithms, secure hardware design. Knowledge of Computer Architecture/Digital Circuit Design/Integrated Circuit Layout/ Hardware Description Language (VHDL/Verilog)/FPGA Architecture/Hardware Security/Machine Learning and Programming skills.	
- NA -			
INR 42,000.00 /- per month (+) HRA @ NA % (if applicable)			
Job Description:			
To design and develop algorithms and methods for detecting hardware threats and preventing hardware attacks.			
Notified Later	Interview details:	-	Department of Computer Science and Engg.
Application link for eligible candidate(s): NIT Rourkela Homepage ⇒ FACULTY & STAFF ⇒ SRICCE ⇒ Career ⇒ Notices			
The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. Last date for submitting the Application: 15-Apr-2026			
The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.			
NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.			
Age Guideline: The upper age limit for applying for the award of project position shall be 28 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.			
Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.			
Tenure of Assignment: 1 Year and can be extended upto 31.03.2029.			
			Sd/- Asst. / Dy. / Jt. Registrar (SR)
Copy to:			
➤ PI & CO- PI: Prof. SHYAMAPADA MUKHERJEE, CS & Prof. SUJATA MAHANTY CS ➤ Chairman, DRC, Dept. of CS			
➤ Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.)			
➤ Dealing Seat (SR – Project Recruitment) ➤ Advertisement File ➔ <i>To publish advertisement at NITR website.</i>			