NITR / SR / 2025 / Advt.- 24CS021 / L / 130   dt. 30-Mar-2026   Ref: FTS 260326-1834   **SR-R03**

Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.

1. **Title of the Project:** Information Security Education and Awareness project Phase III (Hardware Security)

2. **Project Code, Dept. with Closing Date:** SR/24/CS/021 | 31-Mar-2029 | Computer Science and Engg.

3. **Funding Agency Details:** 4197 - CBSD, MEITY [RBI - 10687701200]   -

4. **PI details:** Prof. SHYAMAPADA MUKHERJEE   mukherjees@nitrkl.ac.in, 0661-246 2357
   9434663383   -   - @ gmail.com

5. **Co-PI details:** Prof. SUJATA MAHANTY   sujatam@nitrkl.ac.in, 0661-246 2363
   9437240147   -   - @ gmail.com

6. **Details of the Post(s):**

7. **Educational Qualification & Working Knowledge**

Project Fellow(PF)
[Name of the Post(s)]

04 (Four) *[No. of Post(s)]*   **02** Year(s), **00** Month(s) *(Tenure of Post)*

Year 1 & 2

INR **12,400.00** /- per month
(+) HRA @ **NA** % (if applicable)

- NA -

INR **0.00** /- per month
(+) HRA @ **NA** % (if applicable)

Essential Qualification (s):

The minimum qualification is B.E. / B.Tech. in the CSE/IT/ECE or a related discipline or MCA or M.Sc. in an appropriate branch of Science. The eligible candidates must have secured CGPA not less than 6.5 in the 10 point scale or secured marks not less than 60% in B.E. / B.Tech. or equivalent examination. PCM (Physics, Chemistry & Mathematics) Mandatory in 11th & 12th Standards.

Experience / Software / Skillset & Desirable Qualification:

Gate Qualified.

Job Description:

(1) MS by Research/ M.Tech by Research
(2) Design and implementation of secure hardware architectures on FPGA platforms as
(3) Development and evaluation of machine learning and deep learning models for side-channel attack detection, hardware Trojan identification, and fault injection resilience.

Notified Later | Interview details: -   - -   Department of Computer Science and Engg.

Application link for eligible candidate(s): NIT Rourkela Homepage ➡ FACULTY & STAFF ➡ SRICCE ➡ Career ➡ Notices

The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. **Last date for submitting the Application: 15-Apr-2026**

The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.

NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.

Age Guideline: The upper age limit for applying for the award of project position shall be 28 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.

Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.

Sd/-
Asst. / Dy. / Jt. Registrar (SR)

Copy to:

➤ PI & CO- PI: Prof. SHYAMAPADA MUKHERJEE , **CS** & Prof. SUJATA MAHANTY **CS** ➤ Chairman, DRC, Dept. of **CS**

➤ Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.)

➤ Dealing Seat (SR – Project Recruitment) ➤ Advertisement File → *To publish advertisement at NITR website.*