



# NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India]

Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008

**SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION**

**NITR / SR / 2025 / Advt.- 25EC007 / L / 0034(i) dt. 16/07/2025 Ref: FTS 250609-8125 SR-R03A**

## Corrigendum for Advertisement

Ref.: Advertisement No: NITR / SR / 2025 / Advt.- 25EC007 / L / 0034 dt. 19/06/2025

The last date for receipt of application(s) is extended till the date as mentioned below, with the below corrigendum.

(i) Interview Date on: 13.08.2025.

(ii) Desirable Qualification: Strong knowledge at least in two research areas among the following: 1) Digital signal processing. 2) RF and Microwave Engineering. 3) Wireless communication. 4) Engineering Mathematics, & Experience/ Software skill set: 1) Matlab/Python Programming, 2) FPGA Programming, 3) Machine learning would be a plus.

**Extended Last date: 11/08/2025**

**Asst. / Dy. / Jt. Registrar (SR)**

### **Copy to:**

- PI : Prof. Subrata Maiti , Dept. of EC
- Co-PI : Prof. Shrishailayya M Hiremath, Jogesh Chandra Dash, Dept. of EC
- Chairman, DRC, Dept. of EC
- Head of the Department / Centres / Units ➡ It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Dept. / Centre / Unit.
- Dealing (SR – Project Recruitment) ➡ To publish advertisement at NITR website.
- Advertisement File

**End of Document**



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## SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

NITR / SR / 2025 / Advt.- 25EC007 / L / 034

dt.19-Jun-2025 Ref: FTS 250060-8125

SR-R03

### Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.

1.	Title of the Project:	Waveform Design, Testing, and Verification for Joint Radar and Communication at Millimeter Wave Frequency Band.		
2.	Project Code, Dept. with Closing Date:	SR/25/EC/007	31-Dec-2027	Electronics and Communication Engg.
3.	Funding Agency Details:	Department of Telecommunication (DOT)		
4.	PI details:	Prof. Subrata Maiti 9438519870	-	smaiti@nitrrkl.ac.in, 0661-246 2468 subrata.maiti@gmail.com
5.	Co-PI details:	Prof. Shrishailayya M Hiremath, Prof. Jogesh Chandra Dash 9438519870	-	hiremaths@nitrrkl.ac.in, 0661-246 4691 setshri@gmail.com
6.	Details of the Post(s):			
Project Associate (PA) <i>[Name of the Post(s)]</i>		7. Educational Qualification & Working Knowledge		
02 (Two) <i>[No. of Post(s)]</i>		03 Year(s), 00 Month(s) <i>(Tenure of Post)</i>		
Year 1 & 2		Essential Qualification (s):		
INR 22,500.00 /- per month (+) HRA @ NA % (if applicable)		B.Tech/B.E/B.Sc(Engg) in ECE, EE, EC, EEE, EI, Telecommunication, Applied Electronics, Communication and Networks, CSE or related specializations pertaining to Telecommunication, signal processing, wireless and RF communication and VLSI and Embedded system, Information technology, AI and ML, with minimum 60% marks or 6.5/10 CGPA for throughout the carrier.		
Year 3		M.SC/MCA in Electronics and Communication, Telecommunication, Applied Electronics, Signal processing, Communication, Wireless Communication, RF, Microwave, Radar, VLSI and Embedded system, AI and ML, Computer networks, CSE, any allied specialization, with minimum 60% marks or 6.5/10 CGPA for both postgraduate and undergraduate courses.		
INR 22,500.00 /- per month (+) HRA @ NA % (if applicable)		Experience / Software / Skillset & Desirable Qualification:		
Job Description:		Strong knowledge at least in two research areas among the following: 1) radar signal processing. 2) RF circuit design, debugging, and testing. 3) wireless communication. 4) Understanding of MIMO architecture, beam forming.		
		Experience/ Software skill set: 1) Matlab/Python Programming, 2) Industrial experience with 5G/6G wireless communication, radar, 3) ADS simulation tools, Systemview software, 4) Experience on PCB design would be a plus		

Develop conceptual hardware to prove JRC concepts. Design and development of millimeter wave hardware for 6G wireless communication as well as radar functionality. Testing and debugging RF and baseband circuits. Scope to enrol in M. Tech. Research in the department of Electronics and Communication Engineering (Subject to fulfilment of all Institute/ Department eligibility criteria).

Online Interview details: 15-Jul-2025 10:00 AM Department of Electronics and Communication Engg.

**Application link for eligible candidate(s):** NIT Rourkela Homepage → FACULTY & STAFF → SRICCE → Career → Notices

The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. **Last date for submitting the Application: 13-Jul-2025**

The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.

NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.

**Age Guideline:** The upper age limit for applying for the award of project position shall be 28 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.

**Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.**

Sd/-

Asst. / Dy. / Jt. Registrar (SR)

Copy to:

- PI & CO- PI: Prof. Subrata Maiti, EC & Prof. Shrishailayya M Hiremath, Prof. Jogesh Chandra Dash EC ➤ Chairman, DRC, Dept. of EC
- Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.)
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