

NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India]

Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008 SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

SR-R03

NITR / SR / 2025 / Advt.- 25EC007 dt. 14 - May - 2025 Ref: FTS 250426 - 4105

/L/018

Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.							
1.	Title of the Project:	Waveform Design, Testing, and Verification for Joint Radar and Communication at Millimeter Wave Frequency Band.					
2.	Project Cod	Project Code, Dept. with Closing Date: SR/25/EC/007 31-Dec-2027 Electronics and Communication Engg.					
3.	3. Funding Agency Details: Department of Telecommunication (DOT) -						
4.	PI details:	Prof. Subrata Maiti smaiti@nitrkl.ac.in, 0661-246 2468					
		9438519870		-	sub	rata.maiti @ gmail.com	
5.	Co-PI	Prof. Shrishailayya M Hiremath, Prof. Jogesh Chandra Das		Chandra Dash	hiremath	@nitrkl.ac.in, 0661-246 4691	
	details:	9438519870		-		setshri @ gmail.com	
6. Details of the Post(s): 7. Educational Qualifica				nal Qualification &	Working Kr	iowledge	
Research Fellow (RF)			Essential Qualification (s): M.Tech/M.Tech(R)/MS in Electronics and Communication, Telecommunication, Applied Electronics, Signal processing, Communication,				
[Name of the Post(s)]			Wireless Communication, RF, Microwave, Radar, VLSI and Embedded system, AI and ML , Computer networks, CSE, any allied specialization, with minimum 60% marks or 6.5/10 CGPA for both postgraduate and undergraduate courses.				
01	(One)	03 Year(s), 00 Month(s)	Tech/B.E/B.Sc(Engg) (with valid GATE/NET score) in ECE, EE, EC, EEE, EI, Telecommunication, Applied Electronic Ind Networks, CSE or related specializations pertaining to Telecommunication, signal processing, wireless and RF		communication, Applied Electronics, Communication		
[No. of Post(s)] (Tenure of Post)			VLSI and Embedded system, Information technology, AI and ML, with minimum 60% marks or 6.5/10 CGPA for throughout the carrier. or				
_	[140. 011 031(3)]	(Tenure of Post)				al processing, wireless and RF communication and	
Yea	ar 1 & 2	(Tenure of Post)	VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati	rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF,	with minimum 60% mar ectronics and Commun Microwave, Radar, VI	11 processing, wireless and RF communication and cs or 6.5/18 CGPA for throughout the carrier. lcation, Telecommunication, Applied Electronics, .SI and Embedded system, AI and ML c, Computer	
	ar 1 & 2		VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati	rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF,	with minimum 60% mar ectronics and Commun Microwave, Radar, VI	al processing, wireless and RF communication and s or 6.5/10 CGPA for throughout the carrier. ication, Telecommunication, Applied Electronics,	
	ar 1 & 2 NR 45,000	.00 /- per month	VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati networks, CSE, any allied spec	rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF, ialization, with minimum 60% mar	with minimum 60% mar ectronics and Commun: Microwave, Radar, Vi ks or 6.5/10 CGPA for	11 processing, wireless and RF communication and cs or 6.5/18 CGPA for throughout the carrier. lcation, Telecommunication, Applied Electronics, .SI and Embedded system, AI and ML c, Computer	
	ar 1 & 2 NR 45,000) HRA @ N		VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati networks, CSE, any allied spec	<pre>rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF, ialization, with minimum 60% mar e / Skillset & Desirable</pre>	with minimum 60% mark ectronics and Commun: Microwave, Radar, VI ks or 6.5/10 CGPA for Qualification:	<pre>11 processing, wireless and RF communication and cs or 6.5/10 CGPA for throughout the carrier. ication, Telecommunication, Applied Electronics, .S1 and Embedded system, AI and ML, Computer > both postgraduate and undergraduate courses.</pre>	
 (+ Year	nr 1 & 2 NR 45,000) HRA @ N	.00 /- per month A % (if applicable)	VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati networks, CSE, any allied spec Experience / Softwar Strong knowledge at least	<pre>rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF, ialization, with minimum 60% mar e / Skillset & Desirable : in two research areas amu </pre>	with minimum 60% mark ectronics and Commun: Microwave, Radar, Vi ks or 6.5/10 CGPA for Qualification: ng the following	11 processing, wireless and RF communication and cs or 6.5/18 CGPA for throughout the carrier. lcation, Telecommunication, Applied Electronics, .SI and Embedded system, AI and ML c, Computer	
 (+ Year	NR 45,000) HRA @ N 3 NR 45,000	.00 /- per month	VLSI and Embedded system, Info or M.SC/MCA(with valid GATE/ NET Signal processing, Communicati networks, CSE, any allied spec Experience / Softwar Strong knowledge at least signal processing. 3) wir	<pre>rmation technology, AI and ML, / Any national fellowship) in El on, Wireless Communication, RF, ialization, with minimum 60% mar e / Skillset & Desirable : in two research areas amu reless communication. 4) Ur</pre>	with minimum 60% marfi ectronics and Commun: Microwave, Radar, Vi Ks or 6.5/10 CGPA for Qualification: ng the following derstanding of Mi	 processing, wireless and RF communication and cs or 6.5/10 CGPA for throughout the carrier. ication, Telecommunication, Applied Electronics, S.J and Embedded system, AI and ML, Computer both postgraduate and undergraduate courses. 1) digital signal processing. 2) radar 	

Conduct research on development of joint radar and communication (JRC) technology. Designing, simulating, and testing of waveform for JRC. Design, validate the concept in SDR/ FPGA module. Scope to enrol in Ph.D. in the department of Electronics and Communication Engineering (Subject to fulfilment of all Institute/ Department eligibility criteria).

Walk-in (offline[®] Interview details: 05-Jun-2025 09:00 AM Department of Electronics and Communication Engg.

Application link for eligible candidate(s): NIT Rourkela Homepage ➡ FACULTY & STAFF ➡ SRICCE ➡ Career ➡ Notices

The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. Last date for submitting the Application: 03-Jun-2025

The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.

NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.

Age Guideline: The upper age limit for applying for the award of project position shall be 32 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.

Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.

Sd/-Asst. / Dy. / Jt. Registrar (SR)

Copy to:

, EC & Prof. Shrishailayya M Hiremath, Prof. Jogesh Chandra Dash EC > Chairman, DRC, Dept. of EC PI & CO- PI: Prof. Subrata Maiti

- Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the \geq staff(s) / student(s) working in your Deptt. / Centre / Unit.)
- Dealing Seat (SR Project Recruitment) Advertisement File
- → To publish advertisement at NITR website.
- Page 1 of 1