

NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India] Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008

SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

NITR / SR / 2023 / Advt.- 23EE061 / L / 088 dt.:

BEE061 / L / 088 dt. 13/03/2024 Ref: FTS 2105AA <u>SR-R03</u> Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are									
advised to go through the advertisement details carefully before applying.									
1.	The Of the Project. Management Scheme for Hybrid Storages in On-board Naval Microgrid								
2.			(Closing	(Closing Date): SK/23/EE/061			21/02/2027		
3. Funding Agency Details: DRDO Naval Research Board 4. Department / Centre: Dept. of Electrical Engineering									
4. Department / Centre: Dept. of Electrical Engineering									
5.	Name of the PI	@nitrkl.ac.in	aghos	h.ee @gmail.com	rayp @nit		- @gmail.com		
0.	with details:	0661-246 2				0661-246 2412	T	-	
6.	6. Details of the Post(s):				nal Qualificatio	n & Working Kr			
				Essential Qualification (s): Desirable Qualification:					
Junior Research Fellow (JRF)			ME/MTech in Electrical Engineering with As per DRDO and NIT						
[Name of the Post(s)]			at least 6.5 CGPA or 60% marks or 1st Rourkela norms						
01 (One) 03 Year(s), 00 Month(s)			class in both BE/BTech and ME/MTech +rom						
	- /	()	a recognized Technical Institute or						
[No. o	1.73	re of Post)	Univers:	ity alon	g with Instit	ute norms.			
		er month	Experience	e / Software	/ Skillset:				
	RA @ NA % (if ap		It is desirable that the student should have handsome experience to work in hardware setup fabrication and controller design in microcontroller or DSP platform.						
Year 3			Job Description:						
INR 4	42,000.00 /- pe	A. To carry out the research work (Experimental), in the field of Power Electronics Converters and Controllers Design							
(+) HRA @ NA % (if applicable) (Microcontroller/DSP) & to visit different industry/ institute (in India) and DRDO for progress meeting. B. Assist the PI in preparation of reports related to the research work								-	
C. Other works related to the project ** Scope to enroll as a Ph. D. Scholar in Department of Electrical Engineering (has to satisfy Institute / Department Eligibility Criteria)								Institute /	
Online Interview details: 05/04/2024 10.00 AM Department of Electrical Engineering									
Application link for eligible candidate(s): NIT Rourkela Homepage ⇒FACULTY & STAFF ⇒ SRICCE ⇒ Career ⇒ Notices									
The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks (division (mark sheets and / or certificates) research papers (if any) work experience certificate (if any) etc. This									
indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies									
of application(s) are required to be sent to the Institute.									
Last date for submitting the Application: 31/03/2024									
The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.									
NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised									
qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any									
clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.									
Age Guideline: The upper age limit for applying for the award of project position shall be 28 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs									
(Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.									
Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.									
	Sd/-								
						Asst.	. / Dy. / Jt. Regi	<u>strar (SR)</u>	
Copy to):								
> PI			, Dept. o						
	-PI: Prof. Pravat Kum		, Dept. o	Dept. of EE					
 Chairman, DRC, Dept. of EE Head of the Department / Centres / Units It is requested that the contents of the above advertisement be brought to 									
			the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.						
	ealing Seat (SR – Pro Ivertisement File				ibiish advertisemei	nt at NITR website.			