

NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION

NITR/SR/2022/Advt-22EC016/L/024, dt. 13.06.2022

Applications are invited on prescribed format (On-line) for the temporary post of "Junior Research Fellow (JRF)" for the Project entitled "Design and Development of Environment-friendly, Conformal, Chipless RFID Sensor tag and Compact Broadband Reader Antenna for Intelligent Food Packaging." Selected candidate may enroll in Ph.D. Programme in the Department of Electronics and Communication Engineering, National Institute of Technology Rourkela based on Institute norms and regulations.

		
1.	Project Code	SR/22/EC/016
2.	Name of the Temporary Post	Junior Research Fellow (01)
3.	Name of the Research Project	"Design and Development of Environment-friendly, Conformal, Chipless RFID Sensor tag and Compact Broadband Reader Antenna for Intelligent Food Packaging".
4	Name of the Sponsoring Agency	SERB, DST, Govt. of India
5.	Tenure of the Project	03 years (from 30.05.2022 to 29.05.2025)
6	Tenure of the Assignment	Initial for a period of one year (Extendable to three years upon satisfactory performance)
7.	Job Description	To carry out the research work (simulation, theoretical modeling, and experimental study), in the field of RFID sensor tag and reader antennas.
		Scope to enroll as a Ph. D. Scholar in the Department of Electronics and Communication Engineering (must satisfy Institute / Department Eligibility Criteria)
8.	Consolidated monthly compensation / Fellowship	INR 31,000 (1st & 2nd year) and INR 35,000 (3rd year)
9.	Essential Qualifications and experience	First class B.E. / B. Tech. in Electronics and Communication Engineering or relevant discipline followed by M.E. / M. Tech. / M.S. in relevant discipline of Electronics and Communication Engineering.
10.	Desirable Qualifications / Exp.	M.E. / M. Tech. with specialization RF and Microwave or relevant discipline.
		Candidate should have qualified GATE/NET.
		Working knowledge of simulation and modeling using HFSS/CST/FEKO and MATALAB
11.	Accommodation	Bachelor / Married accommodation in the Institute may be provided subject to availability.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name	: Dr. Situ Rani Patre
Address	: Department of Electronics and Communication Engineering
	National Institute of Technology Rourkela,
	Rourkela- 769 008, Odisha, India.
Mobile	: +91-9340409098
E-mail	: patresr@nitrkl.ac.in

Date and Time for <u>On-line / Offline</u> Interview: Will be notified through email after shortlisting.

Place of Interview: **Department of Department of Electronics and Communication Engineering, NIT Rourkela**

Eligible candidates may apply before 23.06.2022, 23:59 HRS. The candidates are required to send the complete filled up application form (Soft copy) to <u>patresr@nitrkl.ac.in.</u> The application form is available in the following link: <u>http://nitrkl.ac.in/oldwebsite/</u><u>Jobs Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf</u> which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build **a Single PDF** file; it is to be sent to Principal Investigator (<u>patresr@nitrkl.ac.in</u>) on or <u>before 23.06.2022</u>, 23:59 HRS. The candidates are also required to produce relevant documents mentioned in the application form [such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc.] at the time of interview and joining. Selection / Joining will be canceled in case of any suppression of information / document provided earlier.

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

> Sd/-Asst. Registrar (SR)

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on departmental notice boards.
- 2) Dr. Situ Rani Patre, PI with a request to give wide publicity to this advertisement.
- 3) Head of the Department, Electronics and Communication Engineering (ECE).
- 4) Project file.