



**NATIONAL INSTITUTE OF TECHNOLOGY, ROURKELA – 769 008(ODISHA)  
SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION**

**Advt. No.: NITR/SR/2021/Advt.-19EE010/L/018 (i), dt: 03.09.2021**

**[1<sup>st</sup> Modification / Extension Notice against earlier notice vide no. NITR/SR/2021/Advt.-19EE010/L/018, dt: 28.07.2021]**

Applications are invited on prescribed format for the following assignment in a purely time bound research project undertaken in the **Department of Electrical Engineering** of the Institute.

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|-----|--|---|
| 1.  | Name of the Temporary Post                     | Senior Research Fellow (SRF) – 1 No. <b>(Pr. Code: SR/19/EE/010).</b>   |
| 2.  | Name of the Research Project                   | Design, development and prototyping of state of the art Hybrid Energy Storage System (HESS) based high power high speed Electric Vehicle (EV) drive train for a Multi Utility Vehicle (MUV)   |
| 3.  | Name of the Sponsoring Agency                  | SERB-DST (IMPRINT 2)  |
| 4.  | Tenure of the Project                          | 06-Months   |
| 5.  | Tenure of the Assignment                       | 06-Months or Completion of Project whichever is earlier.  |
| 6.  | Job Description                                | Job Description:<br>i) Implementation of Control, Energy Management and Protection algorithms for high power Bidirectional Converters in C2000/DSP/ FPGA platforms<br>ii) Implementation of Battery Management System Algorithms in C2000/DSP/ FPGA platforms<br>iii) Assist the PI/Co-PIs in preparation of reports related to the research work<br>iv) Other works related to the project<br>* No Scope for Ph.D enrolment. |
| 7.  | Consolidated monthly compensation / Fellowship | Rs. 35,000/- per month*   |
| 8.  | Essential Qualifications and experience        | M.Tech. or M.E. in Electrical Engineering. Must have experience to build hardware prototype.  |
| 9.  | Desirable Qualifications/ Experiences          | Writing control algorithms in C2000/DSP/ FPGA for power converter control. Knowledge about different EV simulation platforms.   |
| 10. | Accommodation                                  | Bachelor accommodation in the Institute may be provided subject to availability.  |

\*At present fund exhausted under Manpower Head, so fellowship will be given once 3<sup>rd</sup> year grant received from the sponsoring agency

For technical information on the project, the candidate may contact the Principal Investigator at the following address with an updated Curriculum Vitae:

**Name** : Prof. Susovon Samanta

**Address** : Department of Electrical Engineering,  
NIT Rourkela, Odisha 769 008.

**Telephone No:** 0661-246-2420, 94391 04535

**E-mail** : [samantas@nitrkl.ac.in](mailto:samantas@nitrkl.ac.in)

Date and Time for **walk-in / on-line** Interview: **4<sup>th</sup> October, 2021**. (**Time 10:30 A.M**) Place of Interview: **Department of Electrical Engineering, NIT Rourkela.**

Eligible candidates may apply for walk-in / on-line interview on **24<sup>th</sup> September, 2021**. The candidates are required to send the complete filled up application form (soft copy) to [samantas@nitrkl.ac.in](mailto:samantas@nitrkl.ac.in). The application form is available in the following link [http://nitrkl.ac.in/oldwebsite/Jobs\\_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/oldwebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf) which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build a **single pdf** file and it is to be sent to Principal Investigator ([samantas@nitrkl.ac.in](mailto:samantas@nitrkl.ac.in)) **on or before 24<sup>th</sup> September, 2021**. The candidates are also required to produce relevant documents mentioned in the application form (such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc. during interview.)

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

**Sd/-**  
**Asst. Registrar (SRICCE)**

Copy to:

- 1) All Heads of the Departments, NIT, Rourkela for publication on Departmental Notice Boards.
- 2) Prof. S. Samanta, PI (**Project Code: SR/19/EE/010**) with a request to give wide publicity to advertisement.
- 3) Head of the Department, EE.
- 4) Project file.