



**NATIONAL INSTITUTE OF TECHNOLOGY, ROURKELA – 769 008 (ODISHA)
SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION**

Advt. No: NITR/SR/EC-SMP/2017/223

Dated: 13.07.2017

Walk-in Interview will be conducted for following assignment in a time bound research project undertaken in the Department of Electronics & Communication Engineering of the Institute on **21st July, 2017 and Time 10:00 AM.**

1.	Name of the Temporary Post:	Laboratory Engineer – 01 (One).
2.	Name of the Research Project:	“Special Manpower Development Programme for Chips to System Design (SMDP-C2SD)”
3.	Name of the Sponsoring Agency:	Government of India, Ministry of Communications & Information Technology, Department of Electronics & Information Technology, New Delhi – 110 003.
4.	Tenure of the Project:	5 years. (15 DEC, 2014 to 14 DEC, 2019)
5.	Tenure of the Assignment:	Initial appointment for 12 months and can be extended till project completion based on performance and Institute guidelines.
6.	Job Description:	Taking care of the VLSI Lab including software installation, upgradation.
7.	Consolidated monthly compensation / Fellowship:	Rs. 34,000/- per month
8.	Essential Qualifications and experience:	1 st Class M. Tech. or equivalent in VLSI Design / Microelectronics / Embedded System / ECE / EI / EE related areas with hand on experience on VLSI EDA / TCAD tools and knowledge of Linux operating system and server maintenance.
9.	Desirable Qualifications / Experiences :	Some familiarity with Power Electronics or Signal Processing would be handy.
10.	Accommodation:	Bachelor accommodation in the Institute may be provided subject to availability.

P. T. O.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name: Dr. K. K. Mahapatra
Address: Department of Electronics & Communication Engineering,
National Institute of Technology (NIT), Rourkela – 769 008.
Telephone No: +91-661-246-2454 (Office), Mobile: 9437246541
E-mail: kkm@nitrkl.ac.in

Date and Time for walk-in Interview: **21st July, 2017 at 10:00 Am.**

Place of Interview: **Department of Electronics & Communication Engineering
NIT Rourkela**

Eligible persons should bring the following documents before appearing the interview.

- i) Two passport size colour photographs.
- ii) CV – CV should include contact details containing Present / Permanent Address, Phone No. / Mobile No., e-mail ID, Date of Birth, Qualifications and Experiences, Project / Research Work and other relevant information.
- iii) Photo copies (One Set) of all certificates, mark / grade sheets, experience certificates etc. along-with ORIGINAL documents for verification.
- iv) NO TA/DA will be paid for appearing in the interview.

Note: Personnel appointed against this project post would get an opportunity to pursue Ph. D. / M. Tech. (Research) programme, subject to fulfilling the Institute criteria.

Asst. Registrar (SRICCE)

Copy to:

1. All Heads of the Departments, NIT, Rourkela for publication on Departmental Notice Boards.
2. Dr. K. K. Mahapatra, Principal Investigator with a request to give wide publicity to advertisement.
3. Head of the Department, EC.
4. Project file.